

FIG. 1

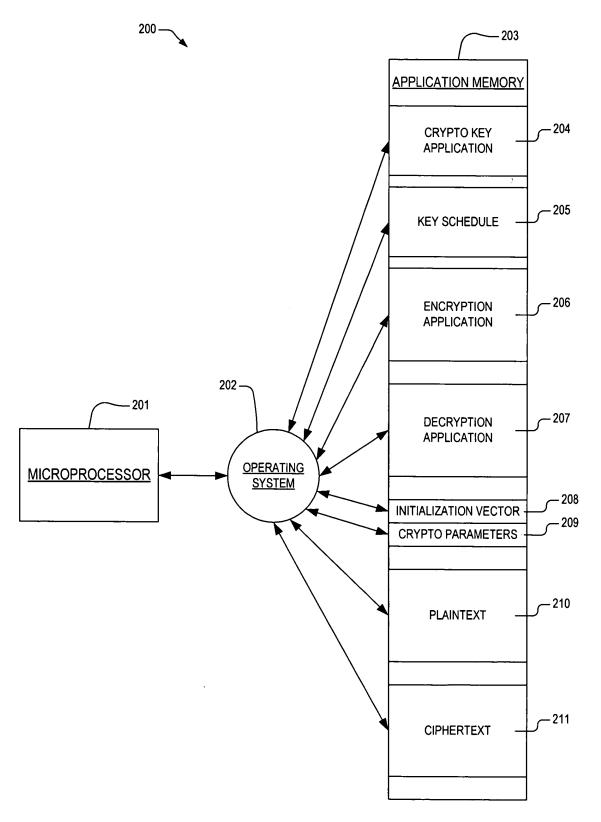


FIG. 2

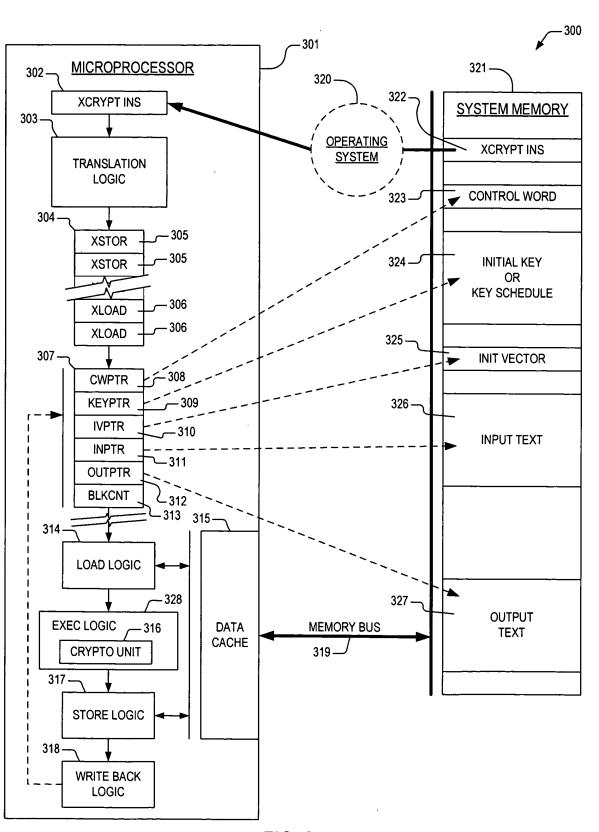


FIG. 3

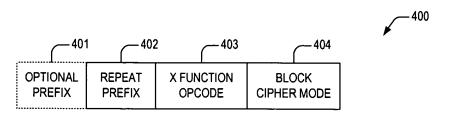


FIG. 4

BCM VALUE	<u>MODE</u>	
0xC8	ELECTRONIC CODE BOOK (ECB)	
0xD0	CIPHER BLOCK CHAINING (CBC)	
0xE0	CIPHER FEEDBACK (CFB)	
0xE8	OUTPUT FEEDBACK (OFB)	
ALL OTHER VALUES	RESERVED	

FIG. 5

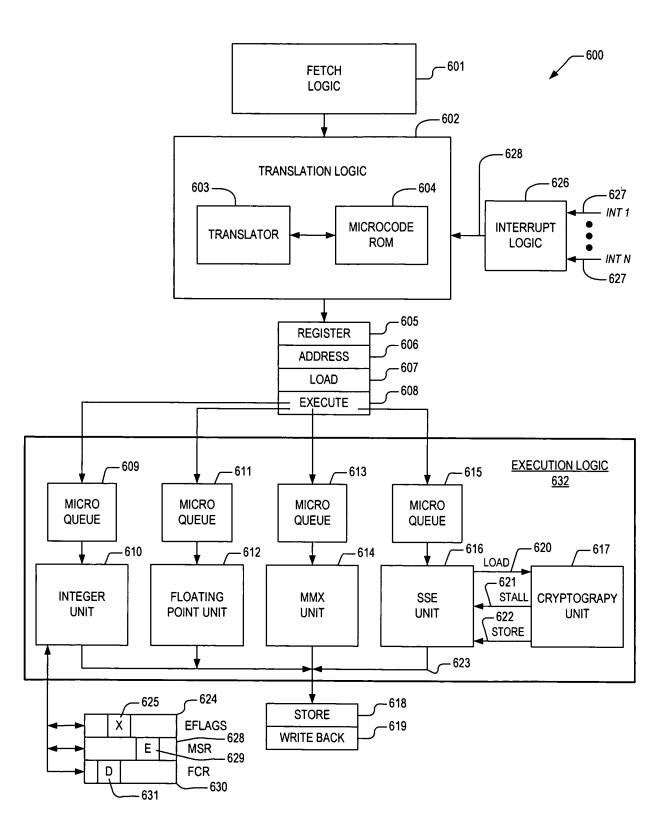


FIG. 6

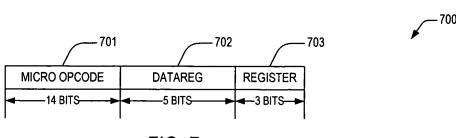


FIG. 7

VALUE	<u>OPERATION</u>	
000	RESERVED	
001	RESERVED	
010	LOAD CONTROL WORD (CW) REGISTER	
011	RESERVED	
100	LOAD INPUT-0 (IN-0) REGISTER AND START CRYPTOGRAPHY UN	
101	LOAD INPUT-1 (IN-1) REGISTER	
110	LOAD CRYPTO KEY-0 REGISTER (LOWER 128 BITS OF KEY)	
111	LOAD CRYPTO KEY-1 REGISTER (UPPER 128 BITS OF KEY)	

FIG. 8

VALUE	<u>OPERATION</u>	
000	RESERVED	
001	RESERVED	
010	RESERVED	
011	RESERVED	
100	STORE OUTPUT-0 (OUT-0) REGISTER	
101	STORE OUTPUT-1 (OUT-1) REGISTER	
110	RESERVED	
111	RESERVED	-

900

FIG. 9

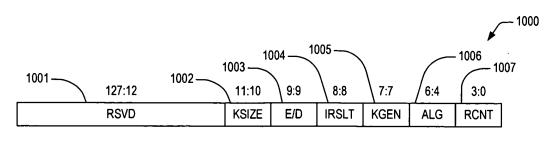
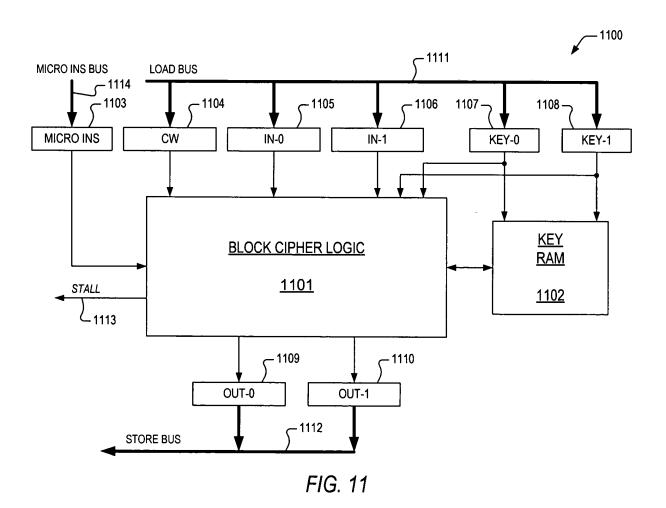


FIG. 10



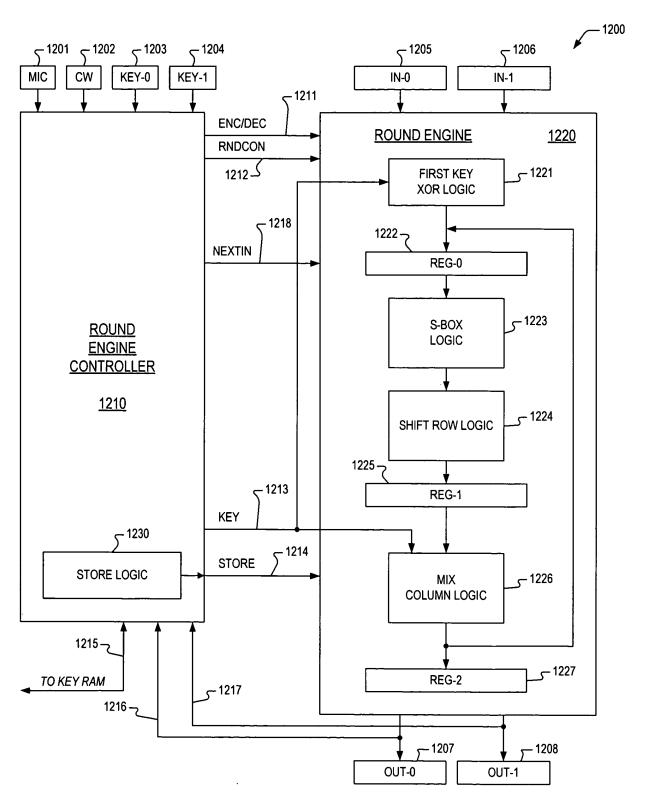


FIG. 12

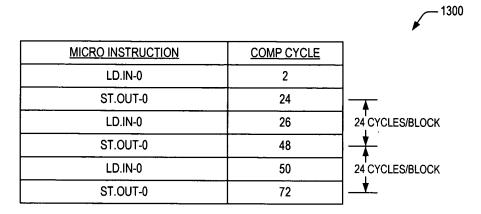


FIG. 13

MICRO INSTRUCTION	COMP CYCLE	7 1400		
LD.IN-0	2	-		
LD.IN-0	4	20 CYCLES/BLOCK		
ST.OUT-0	24			
LD.IN-0	26			
ST.OUT-0	44			
LD.IN-0	46]		

FIG. 14

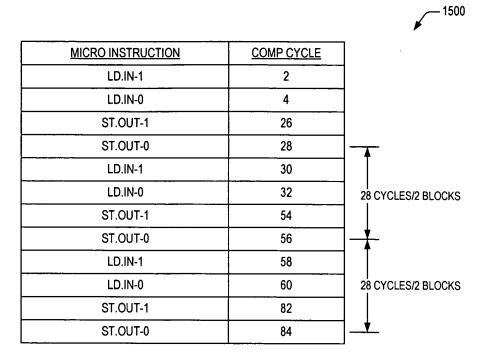


FIG. 15

r		1600
MICRO INSTRUCTION	COMP CYCLE	*
LD.IN-1	2	7
LD.IN-0	4	1
LD.IN-1	6	
LD.IN-0	8]
ST.OUT-1	26	7
ST.OUT-0	28]
LD.IN-1	30	
LD.IN-0	32	20 CYCLES/2 BLOCKS
ST.OUT-1	46	1
ST.OUT-0	48	<u> </u>
LD.IN-1	50	1
LD.IN-0	52	

FIG. 16